



SOT-563 Plastic-Encapsulate Diodes

CESD5V0M5 ESD Protection Diodes

DESCRIPTION

The CESD5V0M5 is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.

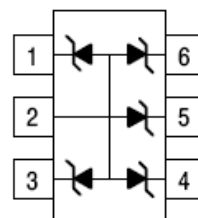
SOT-563



1

FEATURES

- Low Leakage
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- These are Pb-Free Devices



PIN 1. CATHODE
2. ANODE
3 CATHODE
4. CATHODE
5. CATHODE
6. CATHODE

Maximum Ratings @T_A=25°C

Parameter	Symbol	Limits	Unit
IEC61000-4-2(ESD) Air Contact		± 15 ± 15	KV
ESD voltage Per Human Body Model Per Machine Model		16 400	KV V
Peak Power Dissipation @ 8 X 20 ms @T _A ≤ 25°C (Note 1)	P _{pk}	100	W
Steady State Power -- 1 Diode (Note 2)	P _D	150	mW
Thermal Resistance Junction-to-Ambient	R _{ΘJA}	833	°C/W
Lead Solder Temperature – Maximum (10 Second Duration)	T _L	260	°C
Junction and Storage temperature range	T _J , T _{stg}	-55 ~ +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only.

Functional operation above the Recommended. Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted, V_F = 0.9 V Max. @ I_F = 10mA for all types)

Device	Device Marking	V _{RWM} (V)	I _R (μA) @ V _{RWM}	V _{BR} (V) @ I _T		I _T	V _C (V) @Max I _{PP} =1A	V _C (V) @Max I _{PP} =5A	C (pF)
		Max	Max	Min	Max	mA	Max	Max	Typ
CESD5V0M5	52	5.0	5	6.0	7.2	1.0	10.5	13.5	32

1. Non-repetitive current per Figure 1. Derate per Figure 2.

2. Only 1 diode under power. For all 5 diodes under power, P_D will be 20%. Mounted on FR-4 board with min pad.

TYPICAL CHARACTERISTICS

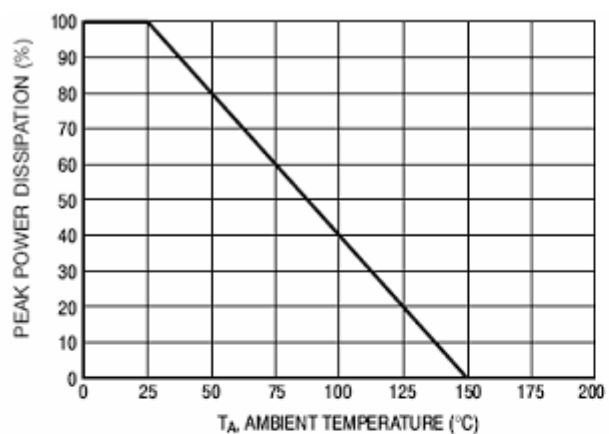


Figure 1. Pulse Derating Curve

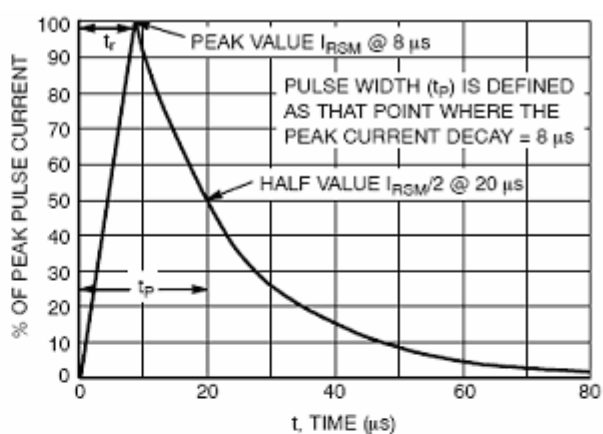


Figure 2. $8 \times 20 \mu\text{s}$ Pulse Waveform

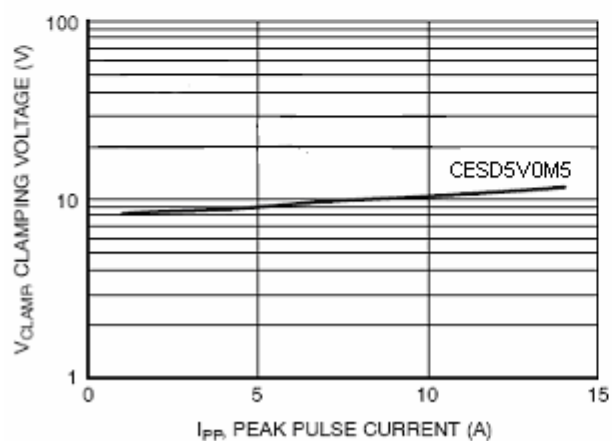


Figure 3. Clamping Voltage vs Peak Pulse Current