

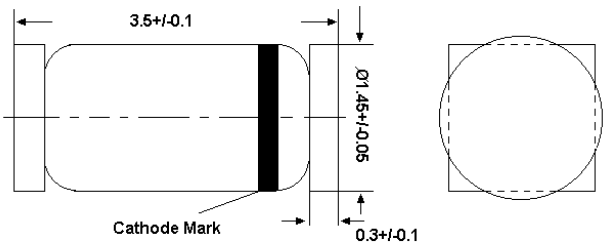
BAV200~BAV203

SILICON EPITAXIAL PLANAR DIODES
Switching Diode

LS-34

Applications:

General Purposes



Glass case MiniMELF

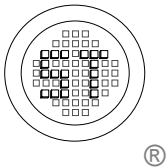
Dimensions in mm

Absolute Maximum Ratings (T_a = 25°C)

	Symbol	Value	Unit
Reverse Voltage	V _R	50	V
BAV200		100	
BAV201		150	
BAV202		200	
Peak Reverse Voltage	V _{RRM}	60	V
BAV200		120	
BAV201		200	
BAV202		250	
Forward Current	I _F	250	mA
Forward Peak Current at f=50Hz	I _{FM}	625	mA
Peak Forward Surge Current at t _p =1s and T _j = 25°C	I _{FSM}	1	A
Junction Temperature	T _j	175	°C
Storage Temperature Range	T _S	-65 to +175	°C

Maximum Thermal Resistance at T_j = 25°C

	Symbol	Value	Unit
Junction ambient at on PC board 50mmx50mmx1.6mm	R _{thJA}	500	K/W



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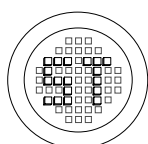


Dated : 14/02/2004

BAV200~BAV203

Characteristics at $T_j = 25^\circ\text{C}$

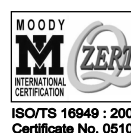
	Symbol	Min.	Typ.	Max.	Unit
Forward Voltage at $I_F = 100\text{mA}$	V_F	-	-	1.0	V
Reverse Current at $V_R = 50\text{V}$ BAV200	I_R	-	-	100	nA
at $V_R = 100\text{V}$ BAV201		-	-	100	nA
at $V_R = 150\text{V}$ BAV202		-	-	100	nA
at $V_R = 200\text{V}$ BAV203		-	-	100	nA
at $V_R = 50\text{V}$, $T_j = 100^\circ\text{C}$ BAV200		-	-	15	μA
at $V_R = 100\text{V}$, $T_j = 100^\circ\text{C}$ BAV201		-	-	15	μA
at $V_R = 150\text{V}$, $T_j = 100^\circ\text{C}$ BAV202		-	-	15	μA
at $V_R = 200\text{V}$, $T_j = 100^\circ\text{C}$ BAV203		-	-	15	μA
Breakdown Voltage at $I_R = 100\mu\text{A}$, $t_p/T = 0.01$, $t_p = 0.3\text{ms}$ BAV200	$V_{(BR)}$	60	-	-	V
BAV201		120	-	-	V
BAV202		200	-	-	V
BAV203		250	-	-	V
Differential Forward Resistance $I_F = 10\text{mA}$	r_f	-	5	-	Ω
Capacitance at $V_R = 0$, $f = 1\text{MHz}$	C_D	-	1.5	-	pF
Reverse Recovery Time at $I_F = 30\text{mA}$, $I_R = 30\text{mA}$, $I_R = 3\text{mA}$, $R_L = 100\Omega$	t_{rr}	-	-	50	ns



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ISO/TS 16949 : 2002
Certificate No. 05103



ISO 14001:2004
Certificate No. 7116



ISO 9001:2000
Certificate No. 0506098

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Characteristics (Tj=25°C unless otherwise specified)

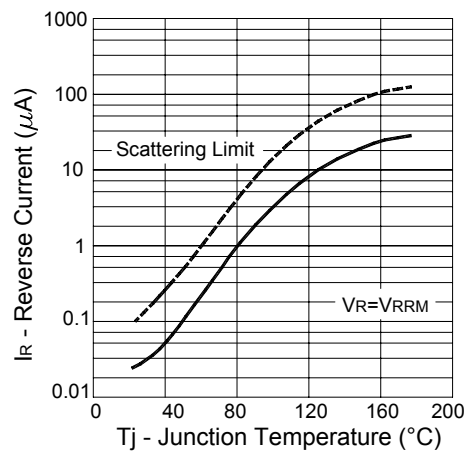


Fig 1. Reverse Current vs. Junction Temperature

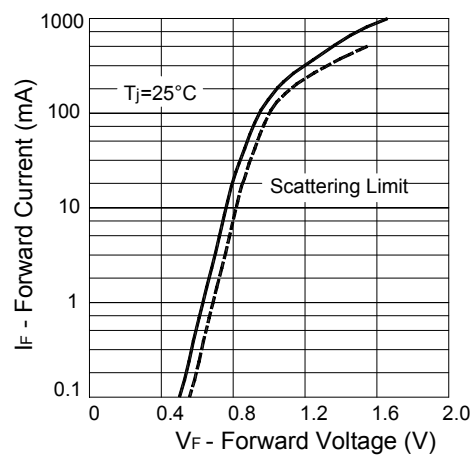
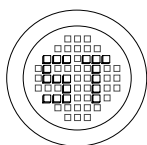


Fig 2. Forward Current vs. Forward Voltage



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