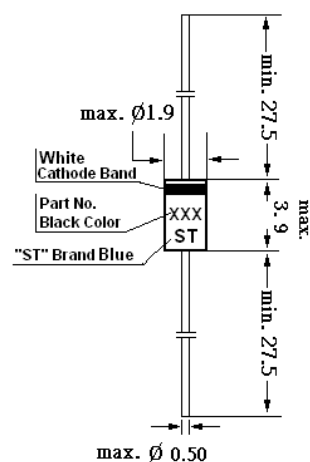


SILICON PLANAR ZENER DIODES

Standard Zener voltage tolerance is $\pm 10\%$. Add suffix "A" for $\pm 5\%$ tolerance. Other tolerances, non standard and higher Zener voltages are upon request.



Glass case JEDEC DO-35

Dimensions in mm

Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

	Symbol	Value	Unit
Zener Current see Table "Characteristics"			
Power Dissipation	P_{tot}	400 ¹⁾	mW
Junction Temperature	T_j	175	$^\circ\text{C}$
Storage Temperature Range	T_s	-65 to +175	$^\circ\text{C}$
¹⁾ Valid provided that leads at a distance of 8mm from case are kept at ambient temperature.			

Characteristics at $T_{\text{amb}} = 25^\circ\text{C}$

	Symbol	Min.	Typ.	Max.	Unit
Thermal Resistance Junction to Ambient Air	R_{thA}	-	-	0.3 ¹⁾	K/mW
Forward Voltage at $I_F = 200\text{mA}$	V_F	-	-	1.2	V
¹⁾ Valid provided that leads at a distance of 8mm from case are kept at ambient temperature.					

TYPE	Zener Voltage Range ¹⁾		Maximum Zener Impedance Z_{ZT} at I_{ZT} Ω	Typical Temperature Coefficient $\%/^{\circ}\text{C}$	Maximum Reverse Leakage Current I_R ²⁾ at $V_R = 1\text{V}$		Maximum Regulator Current I_{ZM} mA
	V_{Znom} V	I_{ZT} mA			$T_a = 25^{\circ}\text{C}$ μA	$T_a = 150^{\circ}\text{C}$ μA	
1N746	3.3	20	28	-0.062	10	30	110
1N747	3.6	20	24	-0.055	10	30	100
1N748	3.9	20	23	-0.049	10	30	95
1N749	4.3	20	22	-0.036	2	30	85
1N750	4.7	20	19	-0.018	2	30	75
1N751	5.1	20	17	-0.008	1	20	70
1N752	5.6	20	11	+0.006	1	20	65
1N753	6.2	20	7	+0.022	0.1	20	60
1N754	6.8	20	5	+0.035	0.1	20	55
1N755	7.5	20	6	+0.045	0.1	20	50
1N756	8.2	20	8	+0.052	0.1	20	45
1N757	9.1	20	10	+0.056	0.1	20	40
1N758	10	20	17	+0.060	0.1	20	35
1N759	12	20	30	+0.060	0.1	20	30

1) Tested with pulses $t_p = 20\text{ms}$

2) Valid provided that leads are kept at ambient temperature at a distance of 8 mm from case.

