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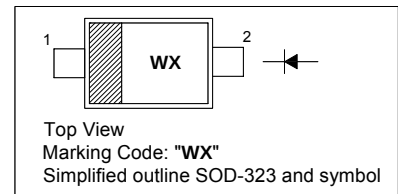
SILICON EPITAXIAL PLANAR DIODE

APPLICATIONS

- High speed switching

PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode

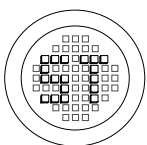


Absolute Maximum Ratings ($T_a = 25^\circ\text{C}$)

Parameter	Symbol	Value	Unit
Peak Reverse Voltage	V_{RM}	85	V
Reverse Voltage	V_R	80	V
Average Forward Current	I_O	100	mA
Peak Forward Current	I_{FM}	300	mA
Non-repetitive Peak Forward Current ($t = 1 \mu\text{s}$)	I_{FSM}	4	A
Junction Temperature	T_J	125	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	- 55 to + 125	$^\circ\text{C}$

Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Max.	Unit
Forward Voltage at $I_F = 10 \text{ mA}$ at $I_F = 100 \text{ mA}$	V_F	0.8 1.2	V
Reverse Current at $V_R = 80 \text{ V}$	I_R	0.1	μA
Capacitance between Terminals at $V_R = 0 \text{ V}$, $f = 1 \text{ MHz}$	C_T	2	pF
Reverse Recovery Time at $V_R = 6 \text{ V}$, $I_F = 10 \text{ mA}$, $R_L = 50 \Omega$	t_{rr}	3	ns



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ISO/TS 16949 : 2002
Certificate No. 05103



ISO 14001:2004
Certificate No. 7116



ISO 9001:2000
Certificate No. 0506098

Dated : 01/09/2005

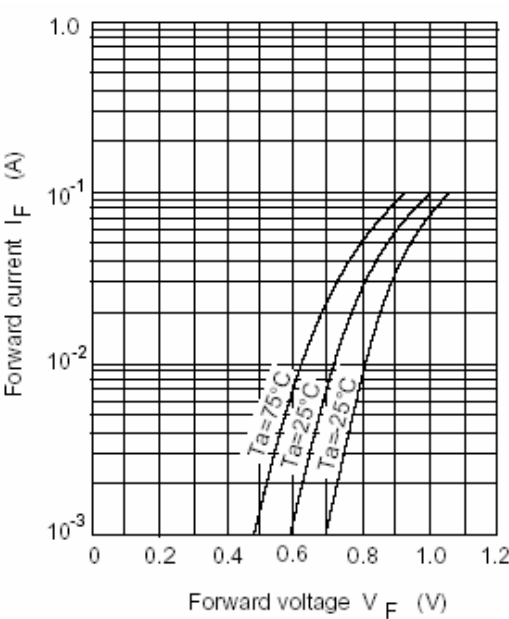


Fig.1 Forward current Vs. Forward voltage

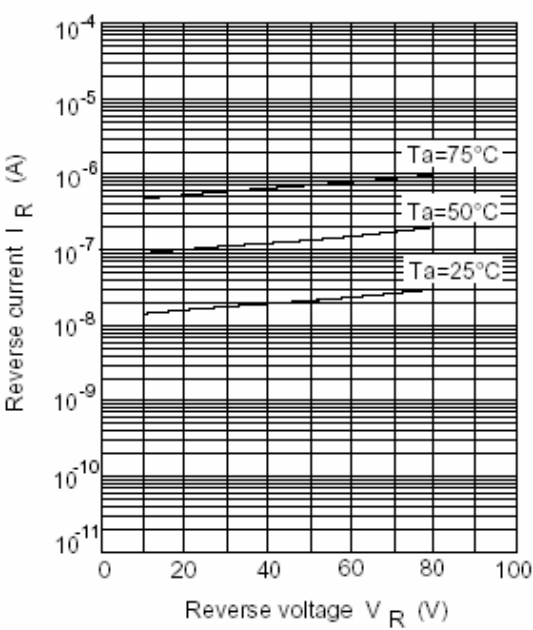


Fig.2 Reverse current Vs. Reverse voltage

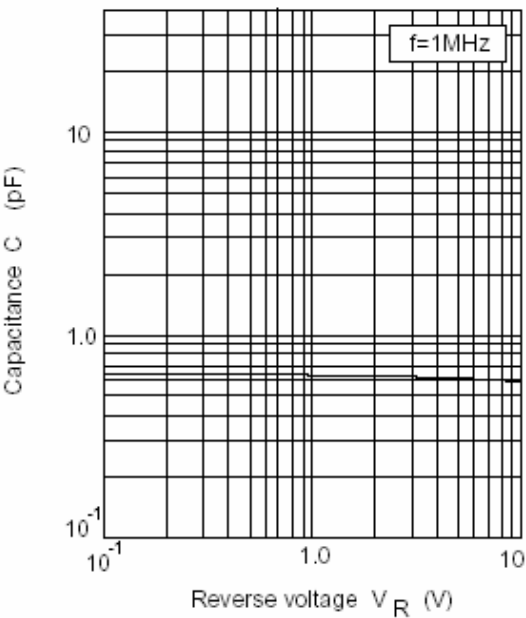
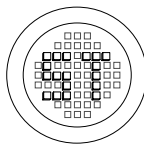


Fig.3 Capacitance Vs. Reverse voltage



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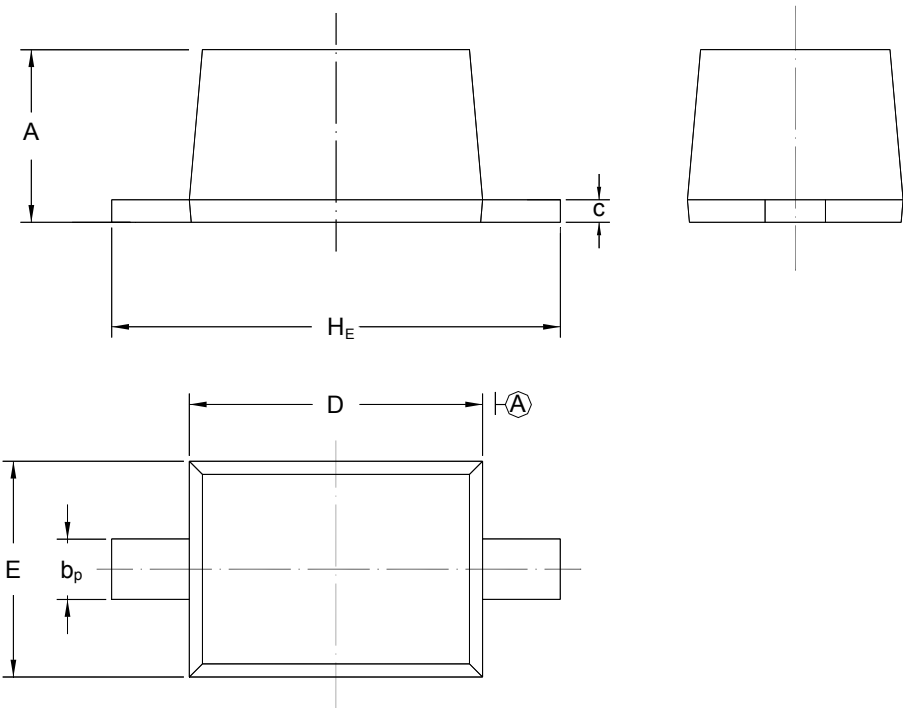


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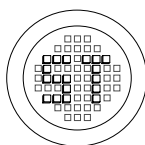
PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SOD-323



UNIT	A	b _p	C	D	E	H _E
mm	1.10 0.80	0.40 0.25	0.15 0.00	1.80 1.60	1.35 1.15	2.80 2.30



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